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Micron Technology Inc.	Boise, Idaho
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Christophe J. Chevallier

Title: SUPPLY VOLTAGE REDUCTION CIRCUIT FOR INTEGRATED CIRCUIT

Docket No.: 703.019US3

Filed: September 18, 2001

Examiner: Edward Wojciechowicz

Customer No.: 21186

Commissioner for Patents

Attn: MAIL STOP ISSUE FEE

P.O. Box 1450

Alexandria, VA 22313-1450

Serial No.: 09/955,270

Due Date: February 3, 2005

Group Art Unit: 2815 Confirmation No.: 8702

Notice of Allowance Date:

November 3, 2004

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